Subject (course) name: Analog Circuits			
Field of study: Electronics and Communications Specialization: all		Subject code: 21K	
		Title graduate: Engineer	
Type of course: major course, obligatory	Course level: First-cycle studies	Year: II Semester: IV Semester: summer	
Form of classes: Lectures, Classes, Labs, Seminar, Project	Number of hours per week: 2L, 1, 2Lab, 0, 0	Credit points: 5 ECTS	

GUIDE TO SUBJECT

SUBJECT OBJECTIVES

- C1. General knowledge of analog electronic circuits.
- C2. Techniques of analysis and design of analog circuits.
- C3. Practical skills in measurement of basic performance metrics of analog circuits.

SUBJECT REQUIREMENTS

- 1. Math fundamentals.
- 2. Basic knowledge of circuit theory.
- 3. Basic knowledge of semiconductor devices
- 4. General ability of individual and group work
- 5. General ability to search in literature

LERNING OUTCOMES

- EK 1 Student will be able to classify basic electronic circuits and explain their principle of operation.
- EK 2 Student will be able to conduct an analysis of basic analog circuits and formulate simple conclusions.
- EK3 Student will be able to design simple and typical analog circuits.
- EK4 Student will be able to conduct the measurements of the basic parameters of analog circuits and process the measurement data.

SUBJECT CONTENT

Form of classes - lectures

Торіс	Hours
W1 – Frequency characteristics of linear systems (Bode asymptotes)	2
W2 – Frequency characteristics of an RC-coupled transistor stage	2
W3 – Operational amplifier; internal structure; basic applications	2
W4 – Analog filters: general classification	2
W5 – Analysis and design of a Sallen-Key filter	2
W6 – Switched capacitor circuits: general information	2
W7 – Feedback in electronic circuits	2
W8 – General properties of circuits with a negative feedback. Stability of analog circuits	2
W9 – AM modulators and demodulators	2
W10 – FM modulators/VCO generators/Mixers	2
W11 – Phase detectors	2

W12 – Phase locked loops; basic characteristics, linear model, PLL's transmittance	2
W13 – Basic applications of phase locked loops	2
W14 – Power amplifiers	2
W15 – Voltage regulators	2
Total	30

Form of classes – exercise classes	
Торіс	Hours
C1 – Frequency characteristics of linear circuits; Bode asymptotes	1
C2 – Frequency analysis of passive RC circuits	1
C3 – Frequency analysis of active linear systems	1
C4 – Design of linear systems	1
C5 – Analysis and design of a second-order Thomas-Tow filter	1
C6 – Transconductance amplifiers and their applications	1
C7 – Test no. 1	1
C8 – Negative feedback in electronic systems	1
C9 – AM modulators	1
C10 – Mixers	1
C11 – Phase detectors	1
C12 – Static characteristics of a PLL circuit	1
C13 – PLL-based FM/PM modulator/demodulator	1
C14 – Voltage regulators	1
C15– Test no. 2	1
Total	15

Form of classes – laboratory

Торіс	Hours
L0 – Introduction	2
L1 – AM modulators	2
L2 – Mixers	2
L3 – Phase locked loops	2
L4 – Basic applications of phase locked loops	2
L5 – Sinusoidal and non-sinusoidal oscillators	2
L6 – Negative feedback	2
L7 – QAM modulators	2
L8 – Frequency synthesizer	2
L9 – Non-ideal op-amps	2
L10 – Voltage comparators	2
L11 – Function generator	2
L12 – Power amplifiers	2
L13 – Spare term	2
L14 – Final class	2
Total	30

STUDY METHODS

1. Lectures

- 2. Exercises analysis and design of electronic circuits
- 3. Laboratory measurement of electronic circuits teamwork

EDUCATIONAL TOOLS

- 1. Textbook with exercises for individual practice
- 2. Laboratory instructions
- 3. Measurement equipment

METHODS OF ASSESMENT (F – Forming, P – Summary)

F1. assessment of laboratory reports

F2. two tests during the course of study

P1. lecture – final test

P2. exercise classes - average result from two tests during the course of study

P3. laboratory – average result from all laboratory reports during the course of study

STUDENT WORKLOAD

Form of activity		Averaged workload (hours)		
		[h]	Σ [h]	ECTS
Participation in class activities	lecture	30		
	exercise classes	15	75	3
	laboratory	30		
Preparation for tutorials (reading literature)		6		
Preparation for lab classes		12		
Preparation of lab reports		12	60	2
Preparation for tests (exercise classes)		12		
Preparation for final exam		18		
Total			135	5

A. BASIC READING

1. T.F. Floyd, D.M. Buchla, "Electronics fundamentals", 8-th ed. Prentice Hall, 2009

2. J.B. Hagen, "Radio frequency electronics: Circuits and applications", 2-nd ed. Cambridge Univ. Press 2009

3. S.A. Pactitis, "Active filters: theory and design", CRC press 2009

B. FURTHER READING

R.J. Baker, "CMOS circuit design, layout and simulation", 2-nd ed, IEEE/Wiley 2008
P.E. Allen, D.R. Holberg, "CMOS analog circuits design" 2-nd ed. Oxford Univ. Press, 2002

Learning objectives	In relation to the learning outcomes specified for the field of study	Subject objectives	Subject content	Course study methods	Methods of assessment
EK1	K_W13 K_U07 K_U09 K_U15	C1, C2	lecture	1	P1
EK2	K_W13 K_U07 K_U09 K_U15	C2	exercise classes	2	F2,P2
EK3	K_W13 K_U07 K_U09 K_U15	C1,C2	lecture, exercise classes	1,2	F2,P2
EK4	K W13	C1.C3	laboratory	3	F1. P3

II. EVALUATION

Grade	Outcome
EK1	Student is able to classify and explain the principle of operation of basic analog circuits
2 (F)	Student is not able to draw a schematic diagram of a circuit, nor explain its principle of operation
3 (E)	Student is able to draw a schematic of a circuit and roughly explain its principle of operation
4 (C)	Student is able to draw a schematic of a circuit and explain its principle of operation giving basic characteristics and formulas
5 (A)	Student is able to draw a schematic of a circuit and explain its principle of operation giving basic characteristics and formulas. Student has a knowledge of second-order effects and possible modifications of the circuit
EK2	Student is able to analyze the operation of basic analog circuits and formulate basic
	conclusions
2 (F)	Student grade for solving a set of exercises is lower than 50%
3 (E)	Student grade for solving a set of exercises is equal to 50%
4 (C)	Student grade for solving a set of exercises is equal to 70%
5 (A)	Student grade for solving a set of exercises is equal to 90%
EK3	Student is able to design simple and typical electronic circuits
2 (F)	Student grade for solving a set of exercises is lower than 50%
3 (E)	Student grade for solving a set of exercises is equal to 50%

4 (C)	Student grade for solving a set of exercises is equal to 70%		
5 (A)	Student grade for solving a set of exercises is equal to 90%		
EK4	Student is able to design the measurements of the basic parameters of analog circuits and		
	process the measurement data		
2 (F)	Student is not able to conduct the correct measurements and calculations		
3 (E)	Student is able to present at least 50% of correct measurements and calculations		
4 (C)	Student is able to present at least 80% of correct measurements, calculations and correct		
	conclusions		
5 (A)	S Student is able to present at least 95% of correct measurements, calculations and correct		
	conclusions		

III. OTHER USEFUL INFORMATION

- 1. All information for students on the schedule are available on the notice board and on the website: <u>www.el.pcz.pl</u>
- 2. Information on the consultation shall be provided to students during the first lecture and will be placed on the website <u>www.el.pcz.pl</u>
- 3. Terms and conditions of credit courses will be provided to students during the first lecture